

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

n re Application of: Chung-Hsiao R. Wu

Serial No.: 10/768,782

Filed: January 30, 2004

For: Dynamic Memory Throttling

For Power and Thermal

Limitations

Group Art Unit: 2827

Examiner: Nguyen, Viet Q.

Attorney Docket: 5681-73900

I hereby certify that this correspondence is being deposited with the U.S. Postal Service as First Class Mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on the date indicated below:

Mario J. Lewin
Name of Registered Representative

Date

## RESPONSE TO OFFICE ACTION MAILED SEPTEMBER 20, 2005

§

§

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

This paper is submitted in response to the Office Action of September 20, 2005.

Please amend the case as follows: